



TCA5550

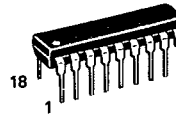
STEREO SOUND CONTROL SYSTEM

The TCA5550 is a single chip stereo balance, volume, bass and treble control circuit designed for use in car radios, TV, and audio systems. Simple dc inputs allow the control to be effected by four inexpensive potentiometers or a remote control system. The bass and treble responses are defined by a single capacitor per control per channel.

- Four High Impedance dc Controls — Vol, Bass, Treble, Balance
- A Single External Capacitor Defines Each Tone Control Characteristic
- Low Distortion, 0.1% at Nominal Input Level, Unity Gain with the Tone Controls Flat
- Channel Separation Better Than 45 dB
- Wide Power Supply Tolerance, 10 to 16 Vdc
- ± 14 dB of Tone Control
- More Than 75 dB of Volume Control
- Wide Dynamic Range: 100 mV to 500 mV_{rms} Input Signal
- Low Output Impedance

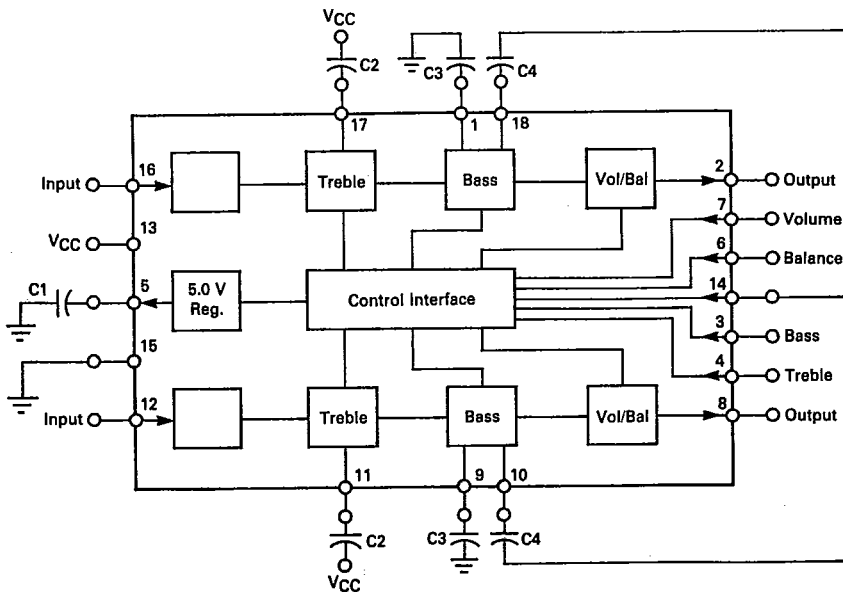
STEREO SOUND CONTROL SYSTEM

SILICON MONOLITHIC INTEGRATED CIRCUIT



P SUFFIX
PLASTIC PACKAGE
CASE 707-02

FIGURE 1 — BLOCK DIAGRAM



MOTOROLA LINEAR/INTERFACE DEVICES

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MAXIMUM RATINGS ($T_A = +25^\circ\text{C}$)

Rating	Value	Unit
Power Supply Voltage	18	Volts
Power Dissipation (Package Limitation) Derate above $T_A = +25^\circ\text{C}$	1250 10	mW mW/°C
Operating Temperature Range (Ambient)	-40 to +85	°C
Storage Temperature Range	-65 to +150	°C

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, $V_{CC} = 12\text{ Vdc}$)

Characteristic	Pin	Min	Typ	Max	Unit
Supply Voltage	13	10	—	16	Vdc
Supply Current	Min Gain Max Gain	—	30	—	mA
		—	15	—	
Regulated Output Voltage ¹ Current	5	—	5.0	—	V mA
		—	—	3.0	
Input Levels	Max Gain With Reduced Gain ³	12, 16	100	—	mV _{rms}
		—	—	500	
Input Impedance	12, 16	—	100	—	kΩ
Output Impedance	2, 8	—	300	—	Ω
Tone Control Range (at 70 Hz & 10 kHz) ² With Pins 3 & 4 @ 0.5 V With Pins 3 & 4 @ 2.3 V With Pins 3 & 4 @ 4.1 V	3, 4	—	-14	—	dB
		—	0	—	
		—	+14	—	
		—	—	—	
Balance Control Range (Constant Power Law) Voltage on Pin 6 for Balanced Gain	6	—	-35	—	dB dB V
		—	+3.0	—	
		—	2.3	—	
Volume Control Range With Pin 7 @ 0 V With Pin 7 @ 3.1 V With Pin 7 @ $V_{PIN 5}$	7	—	80	—	dB
		—	+10	—	
		—	-20	—	
		—	-70	—	
Control Input Currents	3, 4, 6, 7	—	—	±1.0	μA
Channel Separation	—	45	—	—	dB
Distortion (at 1.0 kHz) at 300 mV _{rms} Output ³	—	—	0.1	—	%
Signal : Noise Ratio 50 Hz to 15 kHz, 10 dB Gain, Tone Controls Flat	—	—	70	—	dB
Noise Level 50 Hz to 15 kHz, Min Gain	—	—	50	—	μV _{rms}

NOTES:

1. The control potentiometers to this point, see Figure 7.
2. These figures are functions of the capacitors on Pins 1, 9, 10, 11, 17 & 18. See the application diagram, Figure 7.
3. The input level may be increased to 500 mV_{rms} but the user controls must be adjusted to ensure that the output level does not exceed 300 mV_{rms}.

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PERFORMANCE CHARACTERISTICS, FIGURES 2-7, TAKEN IN CIRCUIT OF FIGURE 8, $V_{CC} = 12\text{ V}$

FIGURE 2 — MIDBAND DISTORTION

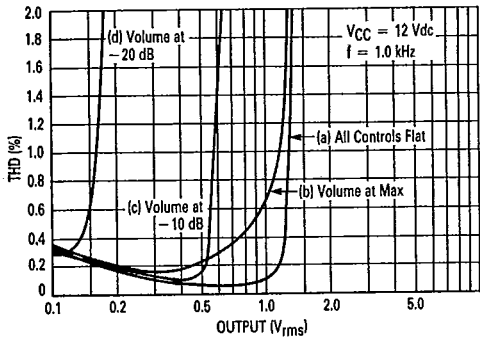


FIGURE 3 — VOLUME CONTROL CHARACTERISTICS

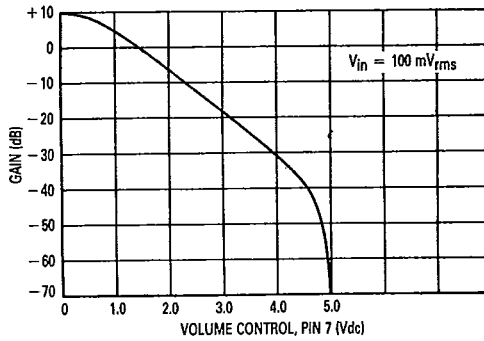


FIGURE 4 — TONE CONTROL CHARACTERISTICS

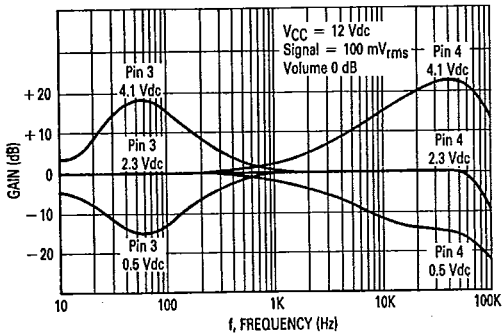


FIGURE 5 — HIGH FREQUENCY DISTORTION

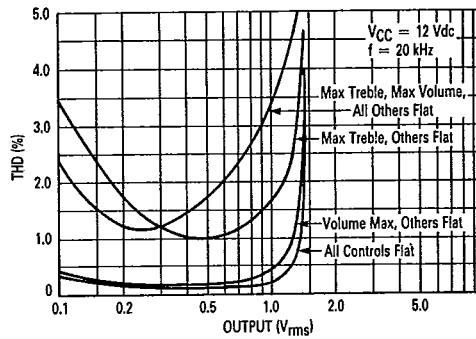


FIGURE 6 — LOW FREQUENCY DISTORTION

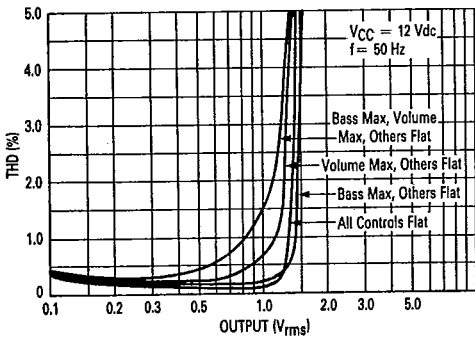
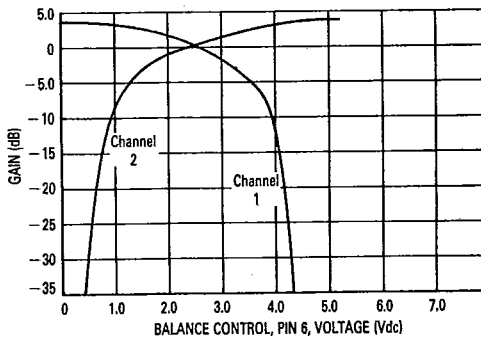


FIGURE 7 — BALANCE CONTROL CHARACTERISTIC

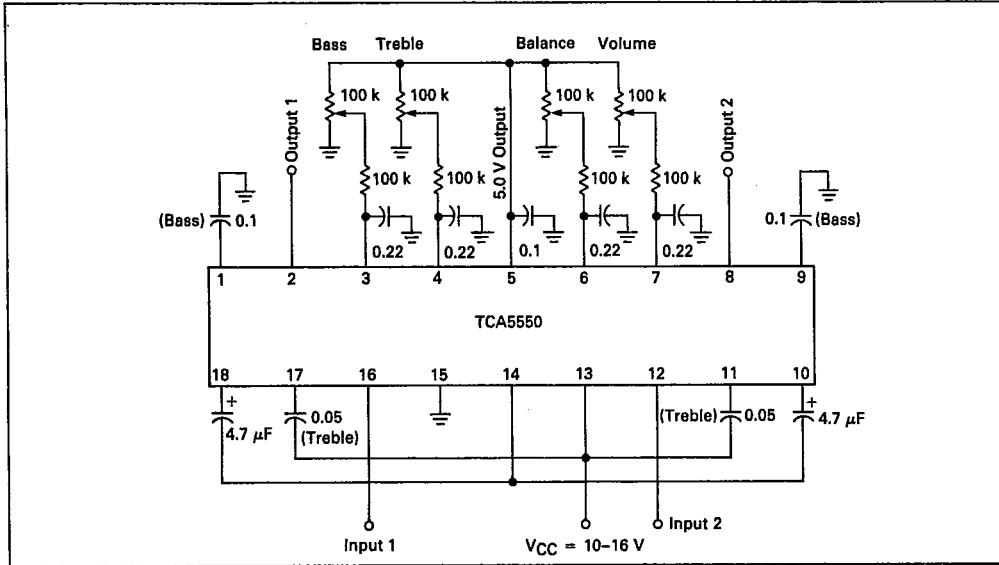


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FIGURE 8 — APPLICATION CIRCUIT



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